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				Paul F-Rauch, Ph.D., Registration No. 38,591 (Depositor's name)				
		(38)						
			_)	4 100	vensa	2004	(Date)	
APPLICATION NO.	FILING DATE	F	FIRST NAMED INVE	NTOR	ATTORNEY	OOCKET NO.	CONFIRMATION NO.	
09/608,158	06/30/2000		Artur E. Balasins	nski 10200/82 1108				
TITLE OF INVENTION: . MASK.	A SCHEME FOR IMPROV	/ING THE SIMUI	LATION ACCURA	CY OF INTEGRAT	ED CIRCUIT P	ATTERNS BY	Y SIMULATION OF THE	
APPLN. TYPE	SMALL ENTITY	ISSUE FE	E P	UBLICATION FEE	TOTAL FE	E(S) DUE	DATE DUE	
nonprovisional	МО	\$1330		\$0	1,370 <u>°°</u>		11/15/2004	
EXAM	IINER	ART UNI	т с	LASS-SUBCLASS		5 10		
THANGAVELU	, KANDASAMY		703-013000	_				
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Number is required.		listed, no name will be printed.						
PLEASE NOTE: Unless	RESIDENCE DATA TO B an assignee is identified be 37 CFR 3.11. Completion of	low, no assignee d	lata will appear on	the patent. If an assi				
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CYPRESS	SEMICONDUCTOR) 01 tr:1301						
Please check the appropriate	assignee category or catego	ries (will not be pri	nted on the patent);	🗖 individual 🧵	corporation or	other private gr	oup entity	
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■ Advance Order - # of Copies								
•	(from status indicated above AALL ENTITY status. See 3	•	□ b. Applicant is no	et claiming SMALL E	NTITY status. S	ee, e.g., 37 CFI	R 1.27(g)(2).	
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